

**WEST****Freeform Search****Database:** All Databases (USPT + EPAB + JPAB + DWPI + TDBD) ▼

11.ti, ab. ▲▼

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ALL	(access???) same l1 same wait state	2	<u>L16</u>
ALL	(access???) same l1 same without same wait state	0	<u>L15</u>
ALL	l12 and l1	0	<u>L14</u>
ALL	l8 same l12	0	<u>L13</u>
ALL	super adj agent	31	<u>L12</u>
ALL	l3 and l10	3	<u>L11</u>
ALL	l9 and l8	46	<u>L10</u>
ALL	l1.ti.ab.	1069	<u>L9</u>
ALL	synchroniz??? same clock same signal	22505	<u>L8</u>
ALL	l6 or l5	45	<u>L7</u>
ALL	shar?? adj4 l1	8	<u>L6</u>
ALL	((arbitration or arbiter or arbitor or arbitrating) same l1)	38	<u>L5</u>
ALL	l3 and l2	1	<u>L4</u>
ALL	((multiple or plurality) adj2 (processor or agent)) or multiprocessor	21770	<u>L3</u>
ALL	synchroniz??? same clock same signal same l1	41	<u>L2</u>
ALL	(synchronous adj2 random access memory)or SDRAM or SSRAM or synchronous memory	2122	<u>L1</u>

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ALL	l30.ti.ab.	18	<u>L31</u>
ALL	dynamic same memory same partition???	79	<u>L30</u>
ALL	l28 same l26	26	<u>L29</u>
ALL	multiprocessor or multiple processor	15335	<u>L28</u>
ALL	l26 and l23	80	<u>L27</u>
ALL	(dynamic???? or adjust???? or program????? or configur????) same (partition??? or allocat???) same memory	1153	<u>L26</u>
ALL	l23 and l13	40	<u>L25</u>
ALL	l21 and l13	4	<u>L24</u>
ALL	l19 or l20 or l21 or l22	1157	<u>L23</u>
ALL	711/17?.ccls.	732	<u>L22</u>
ALL	711/153.ccls.	122	<u>L21</u>
ALL	711/147.ccls.	319	<u>L20</u>
ALL	711/129.ccls.	102	<u>L19</u>
ALL	(register) same (program????? or configur????) same (allocat??? or partition???) same memory	66	<u>L18</u>
ALL	l13 same (processor or agent or cpu or processing element or peripheral)	50	<u>L17</u>
ALL	l9 and l13	87	<u>L16</u>
ALL	l11 and l13	7	<u>L15</u>
ALL	l10 and l13	23	<u>L14</u>
ALL	dynamic???? same l12	256	<u>L13</u>
ALL	(partition??? or allocat???) adj3 memory	3989	<u>L12</u>
ALL	l10 and l9	1893	<u>L11</u>
ALL	memory same bank	14882	<u>L10</u>
ALL	(shar?? or central???? or global??) same memory	22383	<u>L9</u>
ALL	(register same programmable or configur????) same ((allocat??? or partition???) adj3 memory)	26	<u>L8</u>
ALL	(register same programmable or configur????) same (allocat??? or partition???) same memory	112	<u>L7</u>
ALL	l5 and l1	12	<u>L6</u>
ALL	dynamic same memory same (partition??? or allocat???)	1054	<u>L5</u>
ALL	l3 and clock	21	<u>L4</u>
ALL	(arbitrate or arbiter or arbitor or arbitrating or arbitration) and l2	29	<u>L3</u>
ALL	l1.ti.ab.	1069	<u>L2</u>
ALL	(synchronous adj2 random access memory)or SDRAM or SSRAM or synchronous memory	2122	<u>L1</u>